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precomputed jump address

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James, Doug

Mailing address: Carnegie Mellon University Robotics Institute 5000 Forbes Avenue  
Pittsburgh, PA 15213. For more information, see my personal homepage. **Jump ...**  
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BTB; Return Address Stack; **Precomputed** Branch. Recovery logic ... Exceptions are  
usually handled by **jumping** into a service (software) routine. ...

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We designed the entire system, from the processor, to memory and I/O bridge chips, ...

**Branch Prediction**: To help mitigate the effects of the long pipeline ...[www-1.ibm.com/servers/eserver/pseries/hardware/whitepapers/power4.html](http://www-1.ibm.com/servers/eserver/pseries/hardware/whitepapers/power4.html) - 38k -[Cached](#) - [Similar pages](#)**[PS]** Improving Static **Branch Prediction** in a Compiler Brian L. Deitrich ...File Format: Adobe PostScript - [View as HTML](#)This was done to determine if there are any subroutine types that allow a **branch prediction** to be made on a **conditional branch** that guards the subroutine's ...[www.crhc.uiuc.edu/IMPACT/ftp/conference/pact-98-branchpred.ps](http://www.crhc.uiuc.edu/IMPACT/ftp/conference/pact-98-branchpred.ps) - [Similar pages](#)**[PDF]** **Branch** Characterization of an OLTP workload on Pentium IIIFile Format: PDF/Adobe Acrobat - [View as HTML](#)

to reduce I/O wait time. A production ODB run typically. uses hundreds of warehouses

and ... **prediction** experiments. 6.1 **Conditional Branch Prediction** ...

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
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Such hardware allows for improvements of **branch prediction** accuracy, but **branch** ...  
It also often leads to the elimination of **conditional branch** ...

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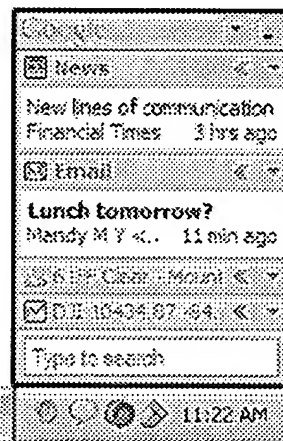
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